



# राष्ट्रीय प्रौद्योगिकी की संस्थान सिलचर National Institute of Technology Silchar

(राष्ट्रीय महत्व का संस्थान)

(An Institute of National Importance)

www.nits.ac.in, Phone: 03842-242914, Fax: 03842-224797, Assam-788010 (INDIA)

Advertisement No.: NITS/Dean-RC/ECE/655/01/2023

Date: 11-12-2023

Applications are invited from the interested and motivated candidates for the post of **Junior Research Fellow (JRF)** in a time bound research project for a temporary period, purely on contractual basis for the CSIR sponsored project in the Department of ECE, NIT Silchar.

Title of the project	Investigation and Analysis of Tunnel Field Effect Transistor (TFET) for the Low Noise, Ultra-low power Analog/RF circuit design
Sponsored Agency	CSIR
Project sanction number	22/0878/23/EMR-II. Dated: 14/07/2023
No. of posts	01 (one)
Project Duration	2 years
Principal Investigator (P.I) Co-investigator	Prof. Srimanta Baishya & Dr. Ujjal Chakraborty

## General Information and Terms & Conditions

1.	Age Limit	28 years
2.	Essential Qualification	B.E/ B Tech in ECE with 55% marks and GATE score or M.E /M. Tech with Gate score
3.	Desirable Experience	Desirable-Knowledge in Device and circuit modeling.
4.	Salary	Rs 31000 p.m
5.	Relaxation on qualification and age	The upper age limit is relax-able up to 5 years for SC/ST/OBC/Women and Physically handicapped candidates
6.	Last date for receiving the filled-up application form by email	<b>25-12-2023</b> Email - <a href="mailto:ujjal@ece.nits.ac.in">ujjal@ece.nits.ac.in</a>
7.	Reporting	Details will be shared through email.
	Job Description	Working as a JRF for the mentioned project
8.	Date of the interview, time, and venue	<b>Date - 29<sup>th</sup> Dec 2023 at 4 PM onward Venue - online</b>
9.	No objection certificate	Employed candidates are to submit a no-objection certificate from his/her employer at the time of the interview.
10.	Institute right	The Institute has the discretion to restrict the number of candidates to be called for the interview on the basis of screening. No candidate may be Recruited if suitable candidate is not found.
11.	Signing of the agreement	Selected candidate shall have to sign an agreement at the time of joining.

Associate Dean (R&C)  
NIT Silchar.

National Institute of Technology Silchar

FORMAT of APPLICATION

For **Junior Research Fellow**

Paste recent  
passport size  
colored  
photograph

1. Advertisement no.
2. Title of the Project: -Investigation and Analysis of Tunnel Field Effect Transistor (TFET) for the Low Noise, Ultra-low power Analog/RF circuit design
3. Department: ECE
4. Name of the applicant (in block letters):
5. Father's/Husband's Name:
6. Date of Birth(DD-MM-YYYY):
7. Age as on **25-12-2023**:
8. Postal Address:

Email:

Phone no.:

9. Permanent Address:
10. Nationality:
11. Marital Status:
12. Whether belongs to SC/ST/PH/OBC/EWS:(If yes, attach certificate):
13. Educational Qualification:(Starting from Class X) (Proof needs to be attached)

Exam. passed	Board/ University	Division	%Age of marks /CPI	Year of passing	Subjects

14. NET/ GATE Examinations Passed (Proof needs to be attached):

I. NET

II. GATE

Subject	Qualifyin gdate	Valid up to	Subject	Score	Valid up to

15. Details of employment (Proof needs to be attached):

Sl. No.	Name of the Organization	Name of the post	Period	Scale of pay and present pay	Nature of duty

16. Any other relevant information that you may like to furnish:

### Declaration

I declare that the above information are true and correct to the best of my knowledge and belief.

Date:

Place:

(Signature of the applicant)